## ADIKAVI NANNAYA UNIVERSITY::RAJAMAHENDRAVARAM III B.TECH – II SEMESTER BTEIE601 MICRO CONTROLLERS AND INTERFACING MODEL QUESTION PAPER

	Time: 3hrs.	Max. Marks: 75			
	SECTION-A (4 X 15 = 60 M) Answer ALL Questions				
1.	a) Explain Embedded systems briefly.	( <b>7M</b> )			
	<b>b</b> ) What are the differences between Microprocessor and Microcontroller.	(8M)			
	Or				
	c) Describe the pin description of 8051.	( <b>8M</b> )			
	<b>d</b> ) Write about RISC and CISC.	(7 <b>M</b> )			
2.	a) Draw the Architecture of 8051.	( <b>8M</b> )			
	<b>b</b> ) Explain the external memory and special function registers?	(7M)			
	Or				
	c) Explain the internal RAM & ROM Organization of 8051.	(7 <b>M</b> )			
	d) Explain different IO ports and CPU register in 8051.	( <b>8</b> M)			
3.	a) What are Arithmetic and logic instructions in 8051.	( <b>8M</b> )			
	<b>b</b> ) Describe Assembly language programming.	(7 <b>M</b> )			
	Or				
	c) What are different addressing modes in 8051.	( <b>8M</b> )			
	d) Describe JUMP loop and CALL instructions.	(7 <b>M</b> )			
4.	a) Explain ADC and DAC interfacing.	( <b>8M</b> )			
	<b>b</b> )Explain interfacing to external memory.	(7M)			
	Or				
	<b>b</b> ) Write about 8051 timer programming.	(8M)			
	c) Explain LCD and Keyboard interfacing.	(7 <b>M</b> )			
	Section-B (5 X 3 =15 Marks)				
5.	Answer any FIVE of the following:				

a) What are the differences between harvard architecture and von Neumann architecture?

- **b**) Draw the pin diagram of 8051 MC.
- c) How many ports are present in 8051 MC and explain them.
- d) What are the special function registers present in 8051.
- e) What are logical operations in C.
- f) Explain data conversion programs in C.
- g) Explain stepper motor interfacing.
- **h**) Explain DC motor interfacing.

## ADIKAVI NANNAYA UNIVERSITY::RAJAMAHENDRAVARAM III B.TECH – II SEMESTER BTEIE602 INDUSTRIAL INSTRUMENTATION-II MODEL QUESTION PAPER

	Time: 3hrs.	-	Max. Marks: 75		
	SECTION-A (4 X 15 = 60 M) Answer ALL Questions				
1.	<b>a</b> ) Describe the	measurement of length using callipers with neat sketch.	(15M)		
	<b>b</b> ) Explain abo	out pneumatic and optical comparators.	(15M)		
2.	<b>a</b> ) How do you	measure density using			
	i) Pı	ressure head type.	( <b>8M</b> )		
	ii) V	ibration type	( <b>7M</b> )		
		Or			
	<b>b</b> ) Explain the	viscosity measurement using			
	i)	Saybolt's viscometer	( <b>8M</b> )		
	ii)	Rotating vane type.	(7M)		
3.	<b>a</b> ) With neat di	agram explain velocity transducers.			
	i)	Electromagnetic type	( <b>8M</b> )		
	ii)	Capacitive type tacho meter	(7 <b>M</b> )		
		Or			
	<b>b</b> ) With neat di	agram explain Acceleration measurement.			
	i)	Potentiometric type	( <b>8M</b> )		
	ii)	Piezo electric type	( <b>7M</b> )		
4.	<b>a</b> ) Explain the	following			
	<b>i</b> )	Ultrasonic leak detectors	( <b>8M</b> )		
	ii)	Thermal conductive leak detectors	( <b>7M</b> )		
		Or			
	b) What is NEMA? Explain NEMA types. (15M)				

#### Section-B (5 X 3 =15 Marks)

#### 5. Answer any FIVE of the following:

- **a**) What is micrometer? Explain.
- **b**) What is Bevel protractor? Explain.
- c) Explain hot wire gas bridge type density method.
- d) Explain friction tube viscometer.
- e) Explain stroboscope for velocity measurement.
- **f**) What is gyro? Explain.
- g) What is limit switches? Explain.
- h) Explain intrinsic safety methods.

## ADIKAVI NANNAYA UNIVERSITY::RAJAMAHENDRAVARAM III B.TECH – II SEMESTER BTEIE603: BIOMEDICAL INSTRUMENTATION MODEL QUESTION PAPER

Time: 3hrs.	Max. Marks: 75
SECTION-A (4 X 15 = 60 M) Answer ALL Questions	
<ul><li><b>a)</b> Explain the components of man-instrument system with a neat of <b>b</b>) Explain about cell composition.</li></ul>	liagram. (10M) (5M)
Or	
c) What are bioelectric potentials? Explain.	(5M)
<b>d</b> ) What are the problems encountered in measuring a timing syste	m. (10M)
<ul><li>2. a) What is electrode? Explain different types of electrodes in biom</li><li>Or</li></ul>	edical instrumentation. (15M)
<b>b</b> ) What are active and passive transducers? Explain with applicati	on and examples. (15M)
<b>3. a)</b> Explain about cardiovascular system.	( <b>8M</b> )
<b>b</b> ) Explain the cardiac output with PQRST curve.	( <b>7</b> M)
Or c) Explain about parameters related to respiratory system. d) Explain about respiratory volume.	(8M) (7M)
<ul><li><b>4.</b> a) Explain about CT scan and MRI scan.</li><li><b>b</b>) Explain about ultrasonic measurement.</li></ul>	(8M) (7M)
Or	
c) Explain about pacemakers and defibrillator.	(8M)
<b>d</b> ) What are the components of biotelemetry system? Explain.	(7 <b>M</b> )
Section-B (5 X 3 =15 Marks) 5. Answer any FIVE of the following:	
<ul><li>a) What are resting and action potential?</li><li>b) Explain about pulse sensors.</li></ul>	
c) Explain about systolic and diastolic pressure.	
d) What are tonometers? Write its uses.	
e) Explain about biomaterials.	
<b>f</b> ) What is telemetry?	
g) Write the different types of electrodes in biomedical industry.	
<b>h</b> ) Explain about x-ray equipment.	

#### ADIKAVI NANNAYA UNIVERSITY::RAJAMAHENDRAVARAM **III B.TECH – II SEMESTER BTEIE604: DIGITAL SIGNAL PROCESSING MODEL QUESTION PAPER**

	Time: 3hrs.Max. Marks: 75	
	SECTION-A (4 X 15 = 60 M) Answer ALL Questions	
1.	a) Find the solution to the following linear convolution difference equation $y(n) - \frac{3}{2}y(n-1) + \frac{1}{2}y(n-2) = (\frac{1}{2})^n$ for $n \ge 0$	
	with initial conditions $y'(-1) = 4$ and $y'(-2) = 10$ b) Explain causality and Stability of a linear time invariant system	(10M) (5M)
	<ul> <li>or</li> <li>c) Determine the frequency response, Magnitude and Phase responses and time delay of the systems given by y(n) = x(n) - x(n - 1) + x(n - 2)</li> <li>d) Derive the relationship between impulse response and frequency response of a discrete time system</li> </ul>	(10M)
2.	<b>a</b> ) Find the DFT of the Following sequence using FFT DIF $X(n) = \{1, 2, 3, 5, 5, 3, 2, 1\}$	(5M) (10M)
	<b>b</b> ) Find the N-Point DFT for $X(n) = a^n for \ 0 < a < 1$ Or	(5M)
3	<ul> <li>c) Compute the DFT of the sequence x(n) = sin[sin nπ/4], where N=8 using DIT FFT</li> <li>d) Given x(n) = {1,2,3,4,4,3,2,1}, find x(k)using DIF FFT algorithm</li> <li>a) Realize the following IIR system functions in the direct form L and II and also Parallel form</li> </ul>	(10M) (5M)
5	$H(Z) = \frac{1}{(1+az^{-1})(1-bz^{-1})}$	(15M)
	<ul><li>b) What is a Keiser window? In what way is it superior to other window functions</li><li>c) Convert the analog filter to digital filter whose system function is</li></ul>	(5M)
	$H(s) = \frac{1}{(s+Z)^2 + (s+1)}$	( <b>10M</b> )
4.	<ul> <li>a) Draw the block diagram of multistage interpolator and explain each block</li> <li>b) Derive an expression for the spectrum of output signal of an decimator</li> <li>Or</li> </ul>	(10M) (5M)
	$e^{-j3w} - 3\pi e^{-3\pi}$	

c) The Desired frequency of a low pass filter is  $H_d(e^{-jw}) = \begin{cases} e^{-3\pi} & \frac{-3\pi}{4} \le w \le \frac{3\pi}{4} \end{cases}$ Determine  $H(e^{jw})$  for M = 7 using rectangular Window. (15M)

#### Section-B (5 X 3 =15 Marks)

#### 5. Answer any FIVE of the following:

1.

- a) Find the power of the signal  $x[n] = f(x) = \begin{cases} 3(-1)^n, \\ 0, \end{cases}$  $n \ge 0$ n < 0
- **b**) What is BIBO stability? What are the conditions of BIBO system?
- c) Show that the following systems are nonlinear and time invariant y(n) x(n)y(n-1) = x(n)
- d) What are the basic building blocks of realization structures?
- e) What are the Advantages of DSP processors in relation to general purpose processors?
- f) State all properties of DFT?
- g) Compare direct form I and direct form II realization of IIR systems?
- **h**) What is the need for multirate signal processing?

### ADIKAVI NANNAYA UNIVERSITY III B.TECH – II SEMESTER ELECTRONICS AND INSTRUMENTATION ENGINEERING BTEIE 605 OBJECT ORIENTED PROGRAMMING THROUGH JAVA MODEL QUESTION PAPER

Max. Marks: 75

		SECTION-A (4 X 15 = 60 M) Answer ALL Questions	
	1.	<b>a</b> ) why we need object oriented programming languages instead of structured programming	ing
		Languages? Explain	(8M)
		<b>b</b> ) How can we say that java is a complete object oriented programming language?	
		Justify your answer.	(7M)
		Or	
		c) Explain the role of command line arguments in java programming	( <b>8</b> M)
		d) Write a java program to find the individual digits of a given number	(7M)
	2.	<b>a</b> ) with the help of syntax, flow chart and example program explain different conditional	
		Statements supported by java	(15M)
		Or	
		<b>b</b> ) Write and explain bitwise, logical and special operator supported by java	(15M)
3.		a) What is an array? Why we need them? list the different types of arrays supported by ja	ava?
		write a java program to multiply two	(15M)
		Or	
		<b>b</b> ) Define a package? write a java program to illustrate the working of packages	( <b>8</b> M)
		c) How can we assign the priorities to a thread? Explain	(7M)
4.		<b>a</b> ) With the help of a neat sketch, explain the exception handling mechanism.	( <b>7</b> M)
		<b>b</b> ) Distinguish between applet and application programming.	( <b>8M</b> )
		Or	
		c) With the help of a neat sketch explain the life cycle of an applet	( <b>8M</b> )
		<b>d</b> ) Write a java program to read information from the keyboard, store it in a file and	
		display the same	( <b>7</b> M)
		Section-B (5 X 3 =15 Marks)	

# to a Short Note on any EN/E of the following

5.	Write a	Shor	t Note	on any	FIVE o	f the following
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Time: 3hrs.

- **a.** Give brief information about the history of java
- **b.** Write a java program to find the factorial value of any given number.
- **c.** Write about the precedence of arithmetic operators
- d. Write a java program to illustrate increment and decrement operators
- e. Describe about the thread synchronization
- f. Describe about the naming conventions used for packages.
- g. Write short notes on types of errors.
- h. Write an applet program to generate cycles and ellipses

## ADIKAVI NANNAYA UNIVERSITY::RAJAMAHENDRAVARAM III B.TECH – II SEMESTER BTEIE 606(a): VLSI DESIGN MODEL QUESTION PAPER

Time: 3hrs.

Max. Marks: 75

#### SECTION-A (4 X 15 = 60 M) Answer ALL Questions

1.	<b>a</b> ) What are the steps involved in the nMOS fabrication? Explain with neat sketches.	( <b>7M</b> )
	<b>b</b> ) Explain the structures of n MOS enhancement mode, depletion mode and p-MOS enhancement mode transistors.	ancement (8M)
	<b>OR</b> <b>b</b> ) Derive the expression for the ratio between Zn u and Zn d if an nMOS inverter is to be	a drivan
	from another nMOS inverter.	( <b>8M</b> )
	c) Draw and explain the operation of BiCMOS inverter.	(7M)
2.	a) What is a stick diagram? Explain about different symbols used for components in stic	k
	diagram.	( <b>8M</b> )
	<b>b</b> ) Draw and Explain about the nMOS inverter design style.	(7M)
	OR	
	c) Explain about Lambda Based Design Rules.	(7M)
	d) Design a stick diagram for 2 input NAND gate by using CMOS Logic.	(8M)
3.	<b>a</b> ). Explain the concept of sheet resistance and apply it to compute the ON resistance (V) of an NMOS inverter having pull up to pull down ratio of 4:1, If n channel resistance is	DD to GND)
	Rsn =10 <sup>4</sup> $\Omega$ per square.	(7M)
	<b>b</b> ) What is inverter delay? How delay is calculated to for multiple stages?	(8M)
	b) Realize basic gates using NMOS	( <b>8M</b> )
	c) Explain the structured design approach of parity generator.	( <b>7M</b> )
4.	a) Explain about building block architecture of FPGA.	( <b>8M</b> )
	<b>b</b> ) Write the VHDL code to implement stack.	(7M)
	OR	
	<b>b</b> ) Write the VHDL code to implement four bit shift register.	( <b>8M</b> )
	c) Explain the design flow using FPGA.	(7M)
	Section-B (5 X 3 =15 Marks)	
5.	Answer any FIVE of the following:	
	a) Compare CMOS, Bipolor, BiCMOS technologies? b) Define the terms figure of merit ( $\omega_0$ ), Pass transistor, $g_{ds}$ .	
	c) Explain how stick diagrams can be used for layout diagrams.	
	d) Design a stick diagram for inverter using CMOS.	
	e) Explain a four line Gray code to Binary code converter.	
	f) Write short notes on switch logic and its arrangements.	
	g) List the applications of FPGA.	
	h) Explain the functions of LUT based logic block.	

### ADIKAVI NANNAYA UNIVERSITY III B.TECH – II SEMESTER ELECTRONICS AND INSTRUMENTATION ENGINEERING BTEIE 606(b) INTRODUCTION TO GEO INFORMATION SYSTEMS MODEL QUESTION PAPER

#### Time: 3hrs. Max. Marks: 75 **SECTION-A** (4 X 15 = 60 M) Answer ALL Questions 3. a) What is Geo Information System? Explain its working Principles and advantages over Convention maps (15M)Or **b**) What is the necessity of Map Projection? Explain different types of Map Projection? (15M) 2. a) What are the Spatial and Non Spatial Data? Explain Data base Structure Models (15M) Or b) What is DBMS? Explain different components of DBMS used in GIS with Schematic Diagram (15M) 3. a) Explain Digitization and Scanning Process ? (15M)Or **b**) Explain Network and Vector Data Analysis? (15M)4. a) Explain about Server side Strategies and mixed strategies (15M) Or b) List out the Applications of GIS? Explain how GIS implemented for solving Multi **Criterion Problems** (15M) Section-B (5 X 3 = 15 Marks) 5. Write a Short Note on any FIVE of the following **a.** What is GIS? Explain its basic function

- b. Write about GIS work flow
- **c.** What are database structure models
- d. Differentiate Attributes and Layers
- e. What are the sources of errors in GIS
- f. What is raster data Analysis
- g. What are the web Applications of GIS
- **h.** Explain GIS based road network Planning

## ADIKAVI NANNAYA UNIVERSITY III B.TECH – II SEMESTER ELECTRONICS AND INSTRUMENTATION ENGINEERING BTEIE 606(c) PC BASED INSTRUMENTATION MODEL QUESTION PAPER

	Time: 3hrs. M	ax. Marks: 75
	SECTION-A (4 X 15 = 60 M) Answer ALL Questions	
1.	<ul> <li>a) Explain about Data Acquisition and control using standard cards</li> <li>Or</li> </ul>	(15M)
	<b>b</b> ) Draw and Explain the Input and Output Modules of PLC	(15M)
2.	a)Explain about Skip and MCR Functions of PLC Or	(15M)
	<b>b</b> ) Explain about Analog PLC Operation	(15M)
3.	a) Write a short notes on characteristics of PID Module and PLC PID Operation <b>Or</b>	on (15M)
	<b>b</b> ) Explain the Process of Controlling of Robot using PLC	(15M)
4.	a)Explain SCADA Architecture in detail Or	(15M)
	<b>b</b> ) Explain SCADA Generations	(15M)
	Section-B (5 X 3 =15 Marks)	
5.	<ul> <li>Write a Short Note on any FIVE of the following</li> <li>a. What is PCI bus</li> <li>b. What are the advantages and disadvantages of PLC</li> <li>c. What is meant by tuning of PID</li> <li>d. Explain Communication technologies used in SCADA</li> <li>e. Explain the networking of PLC</li> <li>f. Differentiate between batch and Continuous Process</li> </ul>	

- g. Explain about different types of timers used in PLC
- **h.** What are the major types of registers in a PLC

## ADIKAVI NANNAYA UNIVERSITY III B.TECH – II SEMESTER ELECTRONICS AND INSTRUMENTATION ENGINEERING BTEIE 606(d) PRINCIPLES AND APPLICATIONS OF NANOTECHNOLOGY MODEL QUESTION PAPER

	Time: 3hrs. Max. N	Iarks: 75
	SECTION-A (4 X 15 = 60 M) Answer ALL Questions	
1.	a) Discuss the classification of Nanomaterials in details.	(15M)
	Or	
	<b>b</b> ) Discuss Bottom up approach of synthesis of Nanomaterials	(15M)
2.	<b>a</b> ) Explain the scope of nanotechnology in electrical, instrumentation, medicine, ae	rospace and
	Material science	(15M)
	Or	
	<b>b</b> ) Explain in detail electric, magnetic, optical properties of nano structured materia	ls. (15M)
3.	<b>a</b> ) Write a notes on fluorescence microscopes	(15M)
	Or	
	<b>b</b> ) Write a notes on Carbon Nano tubes?	(15M)
4.	a) Explain about NEMS sensors in details	(15M)
	Or	
	<b>b</b> ) Explain about Nanoprinting of DNA,RNA and protein	(15M)
	Section-B (5 X 3 =15 Marks)	
5.	Write a Short Note on any FIVE of the following	
	a. Define Nanotechnology	
	<b>b.</b> What is Top down approach?	
	<b>c.</b> What are polymer based nanostructures?	
	d. What is CVD	
	e. Differentiate SEM & TEM	
	f. What is ballistic deflection transistors	
	<b>g.</b> Write short notes on Nanotechnology in cancer treatment	
	<b>n.</b> what is bio sensor	

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